CLAIMS:

What is claimed is:

1. A method comprising:

determining at least one characteristic of a memory request; and

selectively leaving an accessed memory page open after a memory access based, at least in part, on the at least one characteristic for the memory request, to balance memory access latency and bandwidth of a subsequent memory request(s).

2. A method according to claim 1, wherein the at least one characteristic is determined based, at least in part, on whether the memory request or a subset of memory requests are to a single memory page or to more than one memory page.

3. A method according to claim 2, wherein the single memory page is left open after a memory access if the memory request or the subset of memory requests is to the single memory page.

4. A method according to claim 2, wherein the single memory page is closed after a memory access if the memory request or the subset of memory requests is to more than one memory page.

5. A method according to claim 1, wherein the determining at least one characteristic of the memory request is determined based, at least in part, on a type of memory request expected to be received.

6. A method according to claim 5, wherein the type of memory request is an instruction memory request.

7. A method according to claim 6, wherein the instruction memory request results in a page management indicator for leaving the memory page open after the memory access.

8. A method according to claim 5, wherein the type of memory request is a data memory request.

9. A method according to claim 8, wherein the data memory request results in a page management indicator for closing the memory page after the memory access.

10. A method according to claim 1, wherein the at least one characteristic is determined, based at least in part, on an arbitration scheme.

11. A method according to claim 10, wherein the arbitration scheme is based, at least in part, on a priority of a memory request.

12. A method according to claim 11, wherein the priority is based, at least in part, on fairness.

13. A method according to claim 11, wherein the priority is based, at least in part, on quality of service.

14. A method according to claim 1, wherein a memory controller receives the memory request.

15. An apparatus comprising:

a plurality of memory pages; and

a memory controller, coupled with the plurality of memory pages, to analyze at least a subset of received memory requests, to determine whether to selectively leave an accessed memory page open after a memory access based, at least in part, on whether the memory request(s) are to a single memory page or to more than one memory page.

16. An apparatus according to claim 15, the apparatus further comprising a memory to store content, at least a subset of which is executable content; and

a control logic, coupled with the memory, to selectively execute at least a subset of the executable content, to implement an instance of a memory controller.

17. An apparatus according to claim 15, wherein the plurality of memory pages is associated with physical elements of synchronous dynamic random access memory.

18. An apparatus according to claim 15, wherein the determination to selectively leave an accessed memory page open after a memory access is dynamic.

19. An apparatus according to claim 15, wherein a memory controller receives the at least subset of memory requests.

20. A memory controller comprising:

a plurality of memory pages; and

a page manager, coupled with the plurality of memory pages, to selectively leave an accessed memory page open after a memory access based, at least in part, on at least one characteristic for a memory request.

21. A memory controller according to claim 20, the memory controller further comprising a memory to store content, at least a subset of which is executable content; and

a control logic, coupled with the memory, to selectively execute at least a subset of the executable content, to implement an instance of the page manager.

22. A memory controller according to claim 20, wherein the accessed memory page is associated with elements of synchronous dynamic random access memory.

23. A system comprising:

volatile memory, associated with a plurality of memory pages; and

a page manager, coupled with the volatile memory, to selectively leave an accessed memory page open after a memory access based, at least in part, on at least one characteristic for a memory request.

24. A system according to claim 23, wherein the at least one characteristic for the memory request is determined based, at least in part, on a type of memory request expected to be received from an agent making a memory request.

25. A system according to claim 23, wherein the at least one characteristic for the memory request or a subset of memory requests is determined based, at least in part, on whether the memory request or the subset of memory requests are to a single memory page or to more than one memory page.

26. A system according to claim 23, wherein the at least one characteristic is determined, based at least in part, on an arbitration scheme.

27. A system according to claim 26, wherein the arbitration scheme is based, at least in part, on a priority of a memory request.

28. A system according to claim 27, wherein the priority is based, at least in part, on fairness.

29. A system according to claim 27, wherein the priority is based, at least in part, on quality of service.

30. A system according to claim 23, wherein the volatile memory is synchronous dynamic random access memory.

31. A storage medium comprising content, which, when executed by a machine, causes the machine to:

determine at least one characteristic of a memory request; and

selectively leave an accessed memory page open after a memory access based, at least in part, on the at least one characteristic for the memory request, to balance memory access latency and bandwidth of a subsequent memory request(s).

32. A storage medium according to claim 31, wherein the at least one characteristic for the memory request is determined based, at least in part, on a type of memory request expected to be made by an agent making a memory request.

33. A storage medium according to claim 31, wherein the at least one characteristic for the memory request or a subset of memory requests is determined based, at least in part, on whether the memory request or the subset of memory requests are to a single memory page or to more than one memory page.

34. A storage medium according to claim 31, wherein the at least one characteristic is determined, based at least in part, on an arbitration scheme.

35. A storage medium according to claim 34, wherein the arbitration scheme is based, at least in part, on a priority of a memory request.

36. A storage medium according to claim 35, wherein the priority is based, at least in part, on fairness.

37. A storage medium according to claim 35, wherein the priority is based, at least in part, on quality of service.